

In Place of FORM PTO-1449 (Modified)

Serial No.: 10/809,939

Applicant: Christopher L. Hamlin

Filing Date: March 25, 2004

Group: 2825

Atty. Docket No.: 04-0175

**LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANT'S INFORMATION DISCLOSURE
STATEMENT**

Reference Designation

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
AAA						
ABA						
ACA						
ADA						
AEA						
AFA						
AGA						
AHA						
AIA						
AJA						

FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
AKA						
ALA						
AMA						
ANA						
AOA						

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

Examiner

Initial

N Red

APA "On-Chip Networks Weighed a Wiring Alternative" by Ron Wilson, Integrated System Design, June 25, 2001;
www.eetimes.com; News Channels Design Automation;

N Red AQA www.eedesign.com/article/printableArticle.jhtml?articleID=12805718; 6/30/2004/; 2 pages

ANA Sony Computer Entertainment, Inc., IBM and Toshiba join to develop "supercomputer-on-a-chip" for the
Broadband Era; Press Releases, 2 pages; Tokyo, March 12, 2001

N Red ARA Intel® XScale™ Microarchitecture: Product Brief; Copyright 2000 Intel Corporation;
www.intel.com/design/intelxscale/bench.htm; 2 pages; 6/30/2004

Examiner:

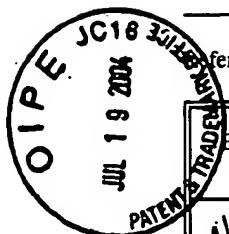
[Signature]

Date Considered:

01/05/06

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through
citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

In Place of FORM PTO-1449 (Modified) Serial No.: 10/809,939
 Applicant: Christopher L. Hamlin
 LIST OF PATENTS AND PUBLICATIONS FOR Filing Date: March 25, 2004
 APPLICANT'S INFORMATION DISCLOSURE Group: 2-825
 STATEMENT Atty. Docket No.: 04-0175



Reference Designation

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
<u>NND</u> AAA	4,656,592	Apr. 7, 1987	Spannenburg, et al.	364	490	Oct. 10, 1984
<u>NND</u> ABA	5,752,070	May 12, 1998	Martin, et al.	395	800.33	Jul. 8, 1994
<u>NND</u> ACA	5,898,677	Apr. 27, 1999	Deeley, et al.	370	276	Jan. 13, 1997
<u>NND</u> ADA	5,940,393	Aug. 17, 1999	Duree, et al.	370	392	May 28, 1996
<u>NND</u> AEA	6,152,613	Nov. 28, 2000	Martin, et al.	395	800.33	Nov. 25, 1997
<u>NND</u> AFA	6,269,277	Jul. 31, 2001	Hershenson, et al.	700	97	Jul. 27, 1998
<u>NND</u> AGA	6,496,508	Dec. 17, 2002	Breuckheimer, et al.	370	397	Nov. 12, 1998
<u>NND</u> AJA						

FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
<u>NND</u> AKA	02202886 JP	Oct. 22, 2001	Japan	G06F0094 4		No
<u>NND</u> ALA	01202397 JP	Jan. 20, 2000	Japan	G06F0175 0	H01L02182	No
AOA						

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

Examiner Initial NNDAPA "Self-Reconfigurable Programmable Logic Device" by Reetinder Sidhu, et al., File #3115; University of Southern California, Office of Technology Licensing, Los Angeles, CA., www.usc.edu/academe/otl; September 7, 2001
NNDAQA "Sea-of-IP: An Ocean of Design Possibilities", Royal Philips Electronics; Philips Semiconductor-Technology Home Pages; www.semiconductor.philips.com/technology/sea-of-ip/index.html; 2/22/02; 3 pages
NNDARA "Tensilica Navigates 'Sea of Processors' Designs" by Chris Edwards; Electronics Times; June 14, 2001; www.eetimes.com, News Channels Semiconductors; 3 pages

Examiner:

[Signature]

Date Considered: 01/05/06

EXAMINER: Initial if reference considered, whether nor not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.